Zaamo and Zalrsc Fast-track extension

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Zaamo/Zalrsc ratification plan

- There is a need to establish official extension names for these two parts of the A extension.
  - A fast track to officially standardize this extension was approved by ARC [1].
  - Zaamo - atomic memory operations
    - Enables microcontroller implementations to implement atomics using the AMO subset
  - Zalrsc - Load-Reserved/Store-Conditional instructions

- Freeze Milestone deliverables:
  1. Specification
  2. Sail
  3. Spike
  4. Qemu
  5. binutils
  6. riscv-config
  7. riscv-arch-test

Draft specification: https://github.com/riscv/riscv-zaamo-zalrsc
Jira: https://jira.riscv.org/browse/RVS-1995
Requesting Tech chairs approval for the Zaamo/Zalrsc extension ratification plan